



**Shri Mata Vaishno Devi University**  
**Consolidated Result of B.Tech. (ECE) – 2<sup>nd</sup> Year, Second Semester, Jan.-May, 2017**  
**Dated: 12/06/2017**

S No.	ENTRY_NO	STUDENT_NAME	CGPA	SGPA	CREDIT_EARNED	BACKLOG_LIST
1.	15BEC017	RATANDEEP SHARMA	8.67	9.13	91	NIL
2.	15BEC007	ASHNA ARYANI	8.34	8.00	91	NIL
3.	15BEC009	GAURAV SAINI	8.13	8.43	91	NIL
4.	15BEC021	SUDANSHU SHARMA	7.92	8.26	91	NIL
5.	15BEC015	NEHA SHARMA	7.35	8.13	91	NIL
6.	15BEC002	AAGMAN JOSHI	7.33	7.22	91	NIL
7.	15BEC001	AMAN KUMAR GARG	7.21	6.87	87	* ECL 2020 Industrial Electronics( 4)
8.	15BEC022	SUDHEER SHARMA	7.06	7.17	80	* ECL 1020 Basic Electrical Engineering( 3) * ECP 1021 Electrical & Electronics Lab( 1) * ECP 1200 Programming using MATLAB( 1) * LNL 1141 Communication Skills( 3) * MEL 1039 Engineering Graphics with CAD( 2) * PCN 1010 NSS( 0) * PHP 1011 Physics Lab( 1)
9.	15BEC011	KARTIK SHARMA	6.81	6.87	91	NIL
10.	15BEC020	SHIVALIK THAKUR	6.53	6.43	91	NIL
11.	15BEC010	KANAV BANDRAL	5.93	6.04	87	* ECL 2020 Industrial Electronics( 4)
12.	15BEC004	ABHISHEK KUMAR MAHAJAN	5.84	5.13	83	* ECL 2020 Industrial Electronics( 4) * ECL 2040 Electromagnetic Field Theory( 4)
13.	15BEC005	AKHIL GORKHA	5.61	4.13	72	* ECL 1022 Network Analysis & Synthesis( 4) * ECL 2020 Industrial Electronics( 4) * ECL 2060 Microprocessor Systems( 4) * ECL 2070 Digital Electronics( 4) * PCL 2042 Introduction to Logic( 3)
14.	15BEC008	DIKSHA BHARTI	5.44	5.00	77	* ECL 1020 Basic Electrical Engineering(

Prepared by

Checked by

Verified by

Sd/-  
JA/OA (AA)

Sd/-  
SO (AA)

Sd/-  
AR (AA)

Sd/-  
I/c Associate Dean (AA)



**Shri Mata Vaishno Devi University**  
**Consolidated Result of B.Tech. (ECE) – 2<sup>nd</sup> Year, Second Semester, Jan.-May, 2017**  
**Dated: 12/06/2017**

						3) * ECL 2020 Industrial Electronics( 4) * ECL 2152 Digital Communication Engineering( 4) * PCL 2042 Introduction to Logic( 3) * PCN 1010 NSS( 0)
15.	15BEC018	RUDARAKSHI SHARMA	5.40	5.26	82	* ECL 1010 Basic Electronics( 4) * MEL 1039 Engineering Graphics with CAD( 2) * PCL 2042 Introduction to Logic( 3) * PCN 1010 NSS( 0)
16.	15BEC003	AAKANKSHA SABHARWAL	5.06	5.22	83	* ECL 1020 Basic Electrical Engineering( 3) * MEL 1039 Engineering Graphics with CAD( 2) * PCL 2042 Introduction to Logic( 3) * PCN 1010 NSS( 0)
17.	15BEC016	RAMESHWAR PRATAP SINGH				RW
18.	15BEC013	MAHIMA GUPTA				RW
19.	15BEC014	NAVITA				WH
20.	15BEC006	AKHIL SHARMA				WH
21.	15BEC019	SAKET PURI				RW

**WH: Result Withheld on account of non-submission of feedback**

Prepared by

Sd/-  
JA/OA (AA)

Checked by

Sd/-  
SO (AA)

Verified by

Sd/-  
AR (AA)

Sd/-  
I/c Associate Dean (AA)