



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY
Consolidated Result of B. Tech (CSE) Batch 2016 7th Semester (Odd semester AY 2019-20)
(Withheld Cases)

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
16BCS053	SHUBHAM UPADHYAY	7	9.12	9.73	162	Nil
16BCS014	HARDIK PADHA	7	9.02	9.32	161	Nil
16BCS027	MOKSH MAHAJAN	7	7.52	7.73	162	Nil
16BCS057	TUSHAR	7	7.41	7.18	162	Nil
16BCS044	SANDEEP KUMAR MISHRA	7	7.27	7.73	162	Nil
16BCS028	MRITUNJAY GOUTAM	7	5.56	6.21	153	* CSP 2031 Data Structure Lab(1) * CSP 2051 Operating System Lab(1) * ECL 2072 Digital Electronics Fundamentals(3) * ECL 2150 Principles of Communication Engineering(3) * ECP 2070 Digital Electronics Lab(1)

Errors and omissions are expected.

Prepared by

JA / SO (Examination)

Checked & Verified by

AR (Examination)

Sign

Faculty I/c. Examination