



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech (ECE) 2015 Batch Students 9th & 10th Semester (Backlog Cases)

Date: 30-12-2020

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
15BEC006	AKHIL SHARMA	9	6.23	7	163	* ECL 2030 Linear Integrated Circuits & Applications(4) * ECL 2071 Digital System Design using VHDL(4)
15BEC014	NAVITA	9	6.22	2.14	134	* CSL 1021 Introduction to C Programming(3) * ECL 1022 Network Analysis & Synthesis(4) * ECL 2030 Linear Integrated Circuits & Applications(4) * ECL 2060 Microprocessor Systems(4) * ECL 2070 Digital Electronics(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 2151 Analog Communication Engineering(4) * ECL 2152 Digital Communication Engineering(4) * ECL 3180 Signal Processing & Linear Systems(4) * ECP 2060 Microprocessor Lab(1) * ECP 2071 VHDL Lab(1)
15BEC019	SAKET PURI	9	5.75	6.24	159	* ECL 2060 Microprocessor Systems(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 3130 IC Fabrication & VLSI(4)
15BEC006	AKHIL SHARMA	10	6.19	5.5	171	Nil
15BEC014	NAVITA	10	6.23	4.23	142	* CSL 1021 Introduction to C Programming(3) * ECL 1022 Network Analysis & Synthesis(4) * ECL 2070 Digital Electronics(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 2151 Analog Communication Engineering(4) * ECL 2152 Digital Communication Engineering(4) * ECL 3180 Signal Processing & Linear Systems(4) * ECP 2060 Microprocessor Lab(1) * ECP 2071 VHDL Lab(1)
15BEC019	SAKET PURI	10	5.72	5.33	171	Nil

E&OE

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam